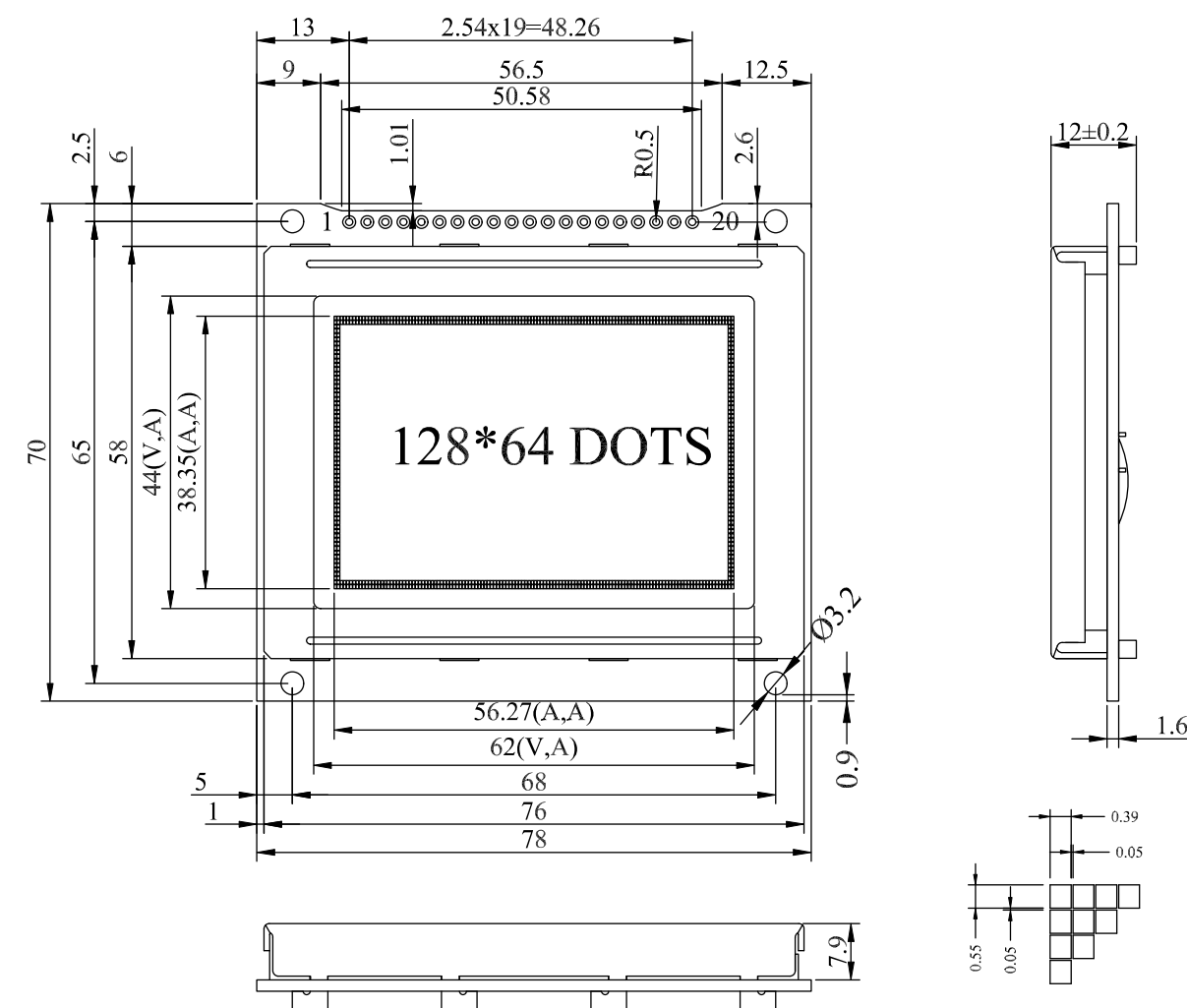




注：在12864TXC-1基础上:1>位孔由Ø3.0改3.2MM

Feature

- 1.POWER SUPPLY: 5.0V AVAILABLE
- 2.LCD TYPE: STN
- 3.1/64 DUTY CYCLE,1/9 BIAS
- 4.VIEWING ANGLE: 6 O'CLOCK
- 5.TEMPERATURE RANGE: EXTENDED(-20--70)
- 6.LOW CONSUMPTION,LOW VOLTAGE,HIGH CONTRAST

Mechanical Data

ITEM	SPECIFICATION	UNIT
DVERALL SIZE		MM
VIEWING AREA		MM
DOT NUMBER		DOTS
DOT SIZE		MM
DOT PITCH		MM

ALL DIMENSIONS ARE IN MM. TOLERANCE IS +/-0.2 UNLESS OTHERWISE NOTED.

Pin Assignment

NO.	SYSBOL	DESCRIPTION
1	CS1	Chip selection for IC1,active"L"
2	CS2	Chip selection for IC2,active"L"
3	VSS	GROUND
4	VDD	POWER SUPPLY FOR LOGIC AND LCD
5	V0	OPERATING VOLTAGE FOR LCD
6	RS	"H"Data ; "L"Instruction code
7	R/W	"H"Read ; "L"Write
8	E	Enable signal
9--16	DB0--DB7	Data bus line
17	/REST/LEDA	Reset signal,active"L"
18	VOU/LEDK	Negative Voltage output
17/19	LEDA	LED BACKLIGHT POWER SUPPLY(+)
18/20	LEDK	LED BACKLIGHT POWER SUPPLY(-)

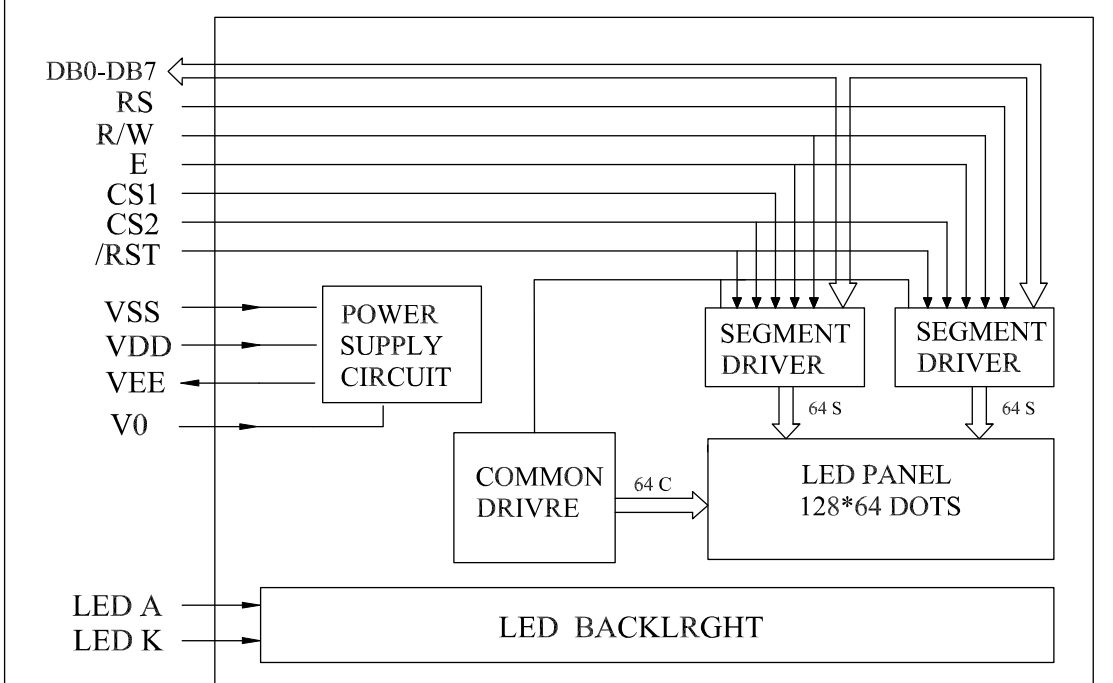
Absolute Maximum Rating

ITEM	SYMBOL	MIN	MAX	UNIT
power supply(logic)	VDD-VSS	4.5	5.5	V
power supply(lcd drv.)	VDD-V0	8	17.0	V
input voltage	Vi	Vi	VDD	V

Electrical Characteristics

ITEM	SYMBOL	MIN	TYP	MAX	UNIT
VOLTAGE FOR LOGIC	VDD-VSS	4.5	5.0	5.5	v
SUPPLY CURRENT	IDD	—	8.0	10.0	mA
LCD OPERATING VOLTAGE	VDD-V0	0C	—	—	v
		25C	—	—	v
		40C	—	—	v
INPUT VOL TAGE	H	VIH	—	vdd	v
	L	VIL	0	—	v

Block Diagram



DESCRIPTION

LCM

OUR DWG NO.

HJ12864TXC-2

DATE

2021-06-23